

## **Abstract:**

The parasitic effects of interconnects could greatly deteriorate the performance of the circuits and lead to many signal integrity problems. Parasitic extraction of interconnects, which builds the equivalent circuit model for interconnects by various numerical approaches, therefore becomes one of the key steps in IC design. A parallel interconnect parasitic extraction tool, ParAFEMImp, is developed to solve the wideband impedance extraction problems. ParAFEMImp is based on PHG and has the potential of good parallel scalability. In this talk, I will show how to use ParAFEMImp and other related packages. And also some experience of developing ParAFEMImp will be introduced, including C/Fortran mixed programming, code optimization and PHG.

**欢迎大家参加！**